

SEP 01 2006

AMENDMENT UNDER 37 C.F.R. § 1.114(c)  
U.S. APPLN. NO.: 10/694,832

ATTY DOCKET NO.: Q76050

**REMARKS**

Claims 1, 3-6, 8-11 are all the claims pending in the application. By this Amendment, claims 2, 7, 12 and 13 are canceled without prejudice or disclaimer.

Claim 1 is amended to include the subject matter of claim 2.

Applicant submits that claim 1 is not anticipated by Tang because Tang fails to disclose a cache module, wherein the cache module comprises a cache controller, the cache controller comprising a data-storing unit on which the read page is written, in combination with other elements of the claim. Although the Examiner argues in the rejection of claim 2 in the Final Office Action that the NAND flash memory corresponds to the data storing unit (p.3), Applicant submits that the NAND flash memory is not a part of the "cache control logic" which the Examiner cites as corresponding to the cache module. Therefore, Tang fails to teach each and every element of claim 1, and claim 1 is allowable.

For reasons similar to those submitted for claim 1, claims 6 and 8 are allowable.

In view of the above, reconsideration and allowance of this application are now believed to be in order, and such actions are hereby solicited. If any points remain in issue which the Examiner feels may be best resolved through a personal or telephone interview, the Examiner is kindly requested to contact the undersigned at the telephone number listed below.

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The USPTC is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.


Respectfully submitted,

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Date: September 1, 2006